

Docket Number: 071469-0303535

PATENT APPLICATION

Client Reference: RAJ-001



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of

DAVID L O'MEARA, et al.

Group Art Unit: 2812

Application No.: 10/630,969

Examiner:

Filed: July 31, 2003

Confirmation No.: 1846

For: FORMATION OF ULTRA-THIN OXIDE LAYERS BY SELF-LIMITING INTERFACIAL OXIDATION

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449s. As this application was filed after June 30, 2003, no copies of the cited U.S. patent or patent publication are attached. However, one copy of each non-patent literature reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed before the mailing date of the first Office Action on the merits in the present application. No certification or fee is required.

Respectfully Submitted,

Dale S. Lazar
Registration Number 28872

Date: January 8, 2004

PILLSBURY WINTHROP LLP
Telephone: (703) 905-2000
Facsimile: (703) 905-2500
P.O. Box 10500
McLean, VA 22102

Approved for use through 07/31/2006. OMB 0651-0031

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of
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Complete if Known

Application Number	10/630.969
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Filing Date	07/31/2003
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First Named Inventor	DAVID J. O'MEARA
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Art Unit	2812
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Examiner Name

Attorney Docket Number	071469-0303535
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U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

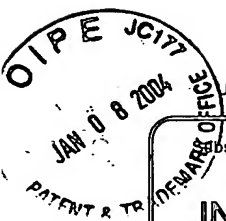
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		Application Number	10/630,969		
		Filing Date	07/31/2003		
		First Named Inventor	DAVID L O'MEARA		
		Art Unit	2812		
		Examiner Name			
Sheet	1	of	1	Attorney Docket Number	071469-0303535

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	A	LIU, H.I. ET AL., "Self limiting oxidation for fabricating sub-5nm silicon nanowires," Appl. Phys. Letter, American Institute of Physics, Vol. 64 (No. 11), p. 1383-1385, (March 11, 1994).	
	B	LIU, H.I. ET AL., "Self-limiting oxidation of Si nanowires," J.Vac. Sc. Technol. B, American Vacuum Society, Vol. 11 (No. 6), p. 2532-2537, (Nov/Dec 1993).	
	C	FUKUDA, HIROSHI ET AL., "Fabrication of silicon nanopillars containing polycrystalline silicon/insulator multilayer structures," Appl. Phys. Lett., American Institute of Physics, Vol. 70 (No. 3), p. 333-335, (January 20, 1997).	
	D	ROY, P.K., "Gate Dielectrics," p. 276-289.	

Examiner Signature		Date Considered	
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